



Form PTO 1449  U.S. Department of commerce Patent and Trademark Office  Information Disclosure Statement by Applicant	ATTY. DOCKET NUMBER	SERIAL NUMBER
	HITA.0474	<del>To be assigned</del> 10/738,544
	APPLICANT	
	ODA et al.	
	FILING DATE	GROUP
	12/18/2003 <del>Concurrently herewith</del>	2823





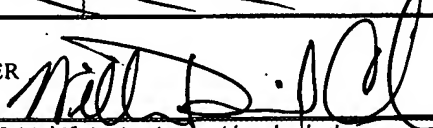
#### U.S. Patent Documents

Examiner Initial		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	**	6,350,993	02/26/2002	Chu et al.	257/192		03-12-1999

#### Foreign Patent Documents

Examiner Initial		DOCUMENT NUMBER	FILING DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION	
							YES	No
	**	0307850	09-13-1988	EPO			Abstract	X

#### Other Documents (Including Author, Title, Date Pertinent Pages, Etc.)

	*	Konig et al., "p-Type SiGe Channel Modulation Doped Field-Effect Transistors with Post-Evaporation Patterned Submicrometre Schottky Gates," IEEE Electronics Letters, Vol. 29, No. 5, March 4, 1993, pp. 486-688
	*	Konig et al., "p-Type Ge-Channel MODFET's with High Transconductance Grown on Si Substrates," IEEE Electron Device Letters, Vol. 14, No. 4, April 1993, pp. 205-207
	*	Konig et al., "Enhancement Mode n-Channel Si/SiGe MODFET with High Intrinsic Transconductance," IEE Electronics Letters, Vol. 28, No. 2, January 16, 1992, pp. 160-162
	*	Sadek et al., "Design on Si/SiGe Heterojunction Complementary Metal-Oxide-Semiconductor Transistors," IEEE Transactions on Electron Devices, Vol. 43, No. 8, August 1996, pp. 1224-1232
EXAMINER 	DATE CONSIDERED 3/3/2005	
Examiner: Initial if citation is considered, whether or not citation is in conformance with MPEP 609. Draw a line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant		

PTO 1449

- \* Cited by Applicant in Parent
- \*\* Cited by Examiner in Parent